**ECEGR 2220: Microprocessor Design**

**Spring 2018**

**LAB 2 REPORT**

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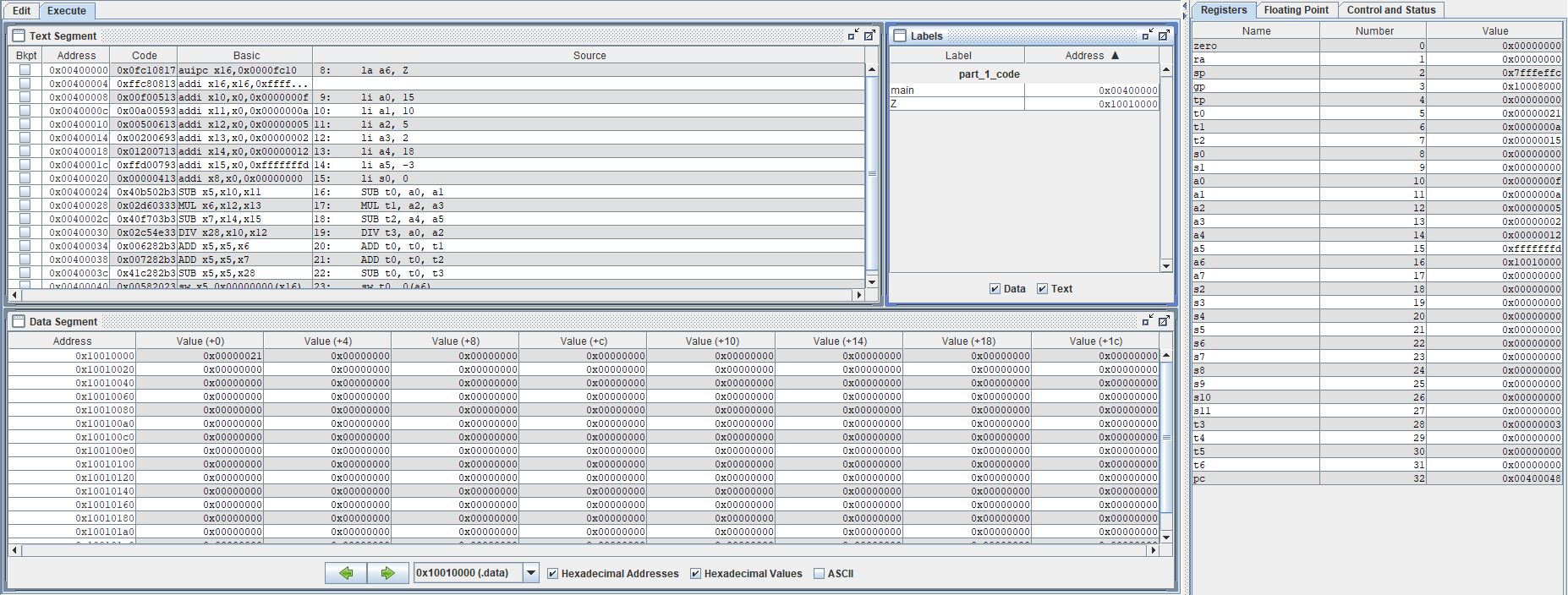
**Performed by:**

**Don-Thuan Le - Thanh Nguyen – Lauren Molina**

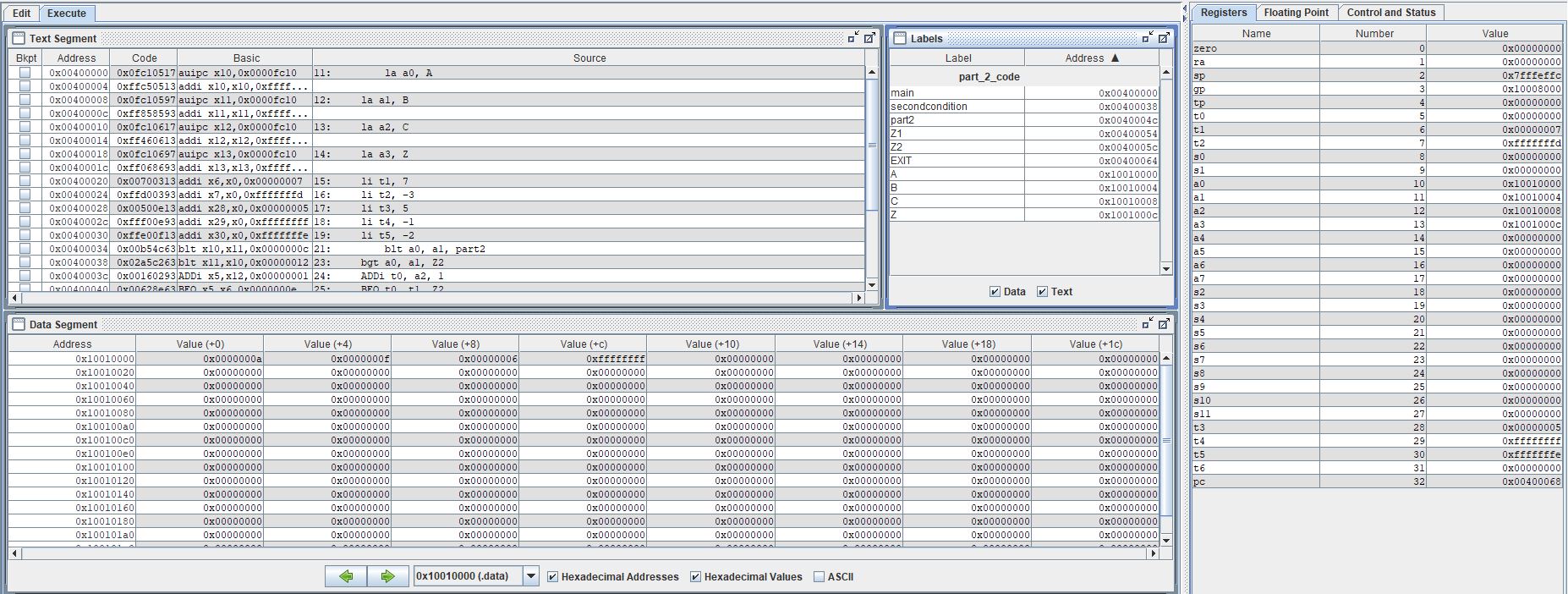
**Date of Report: 04/28/2018**

**SEATTLE UNIVERSITY**

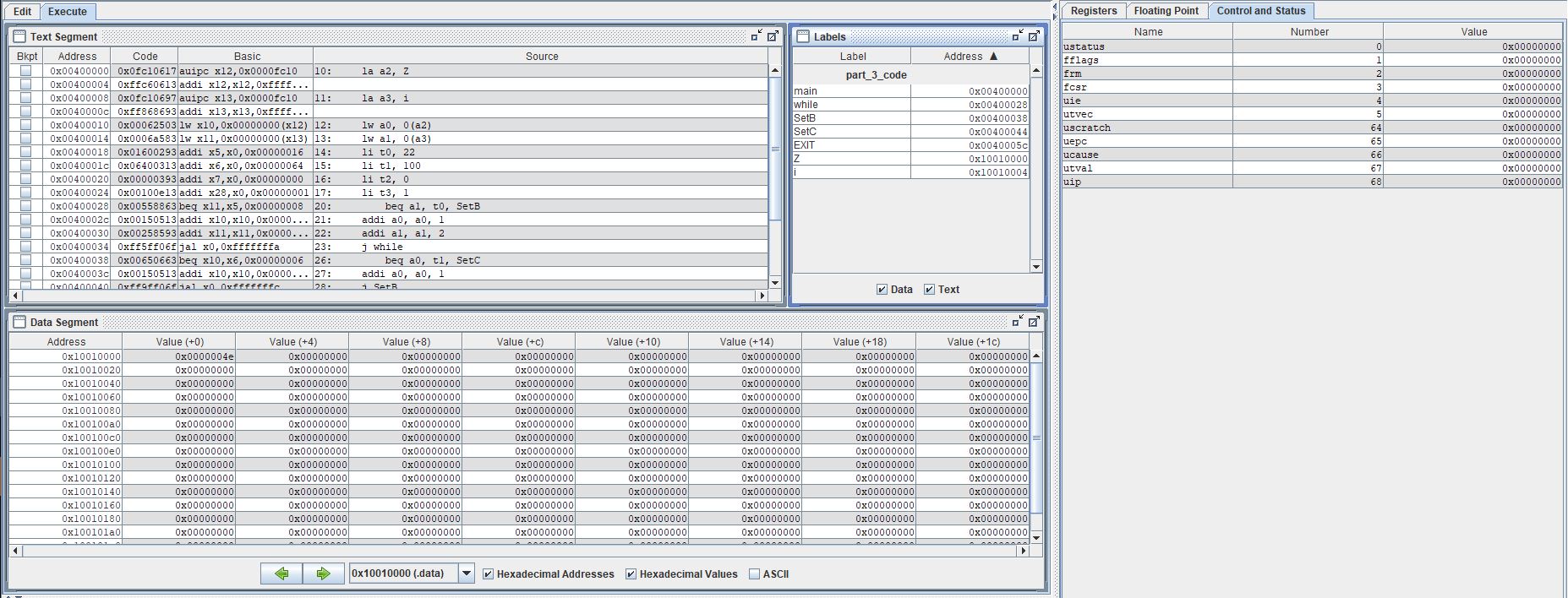
**Department of Electrical and Computer Engineering**



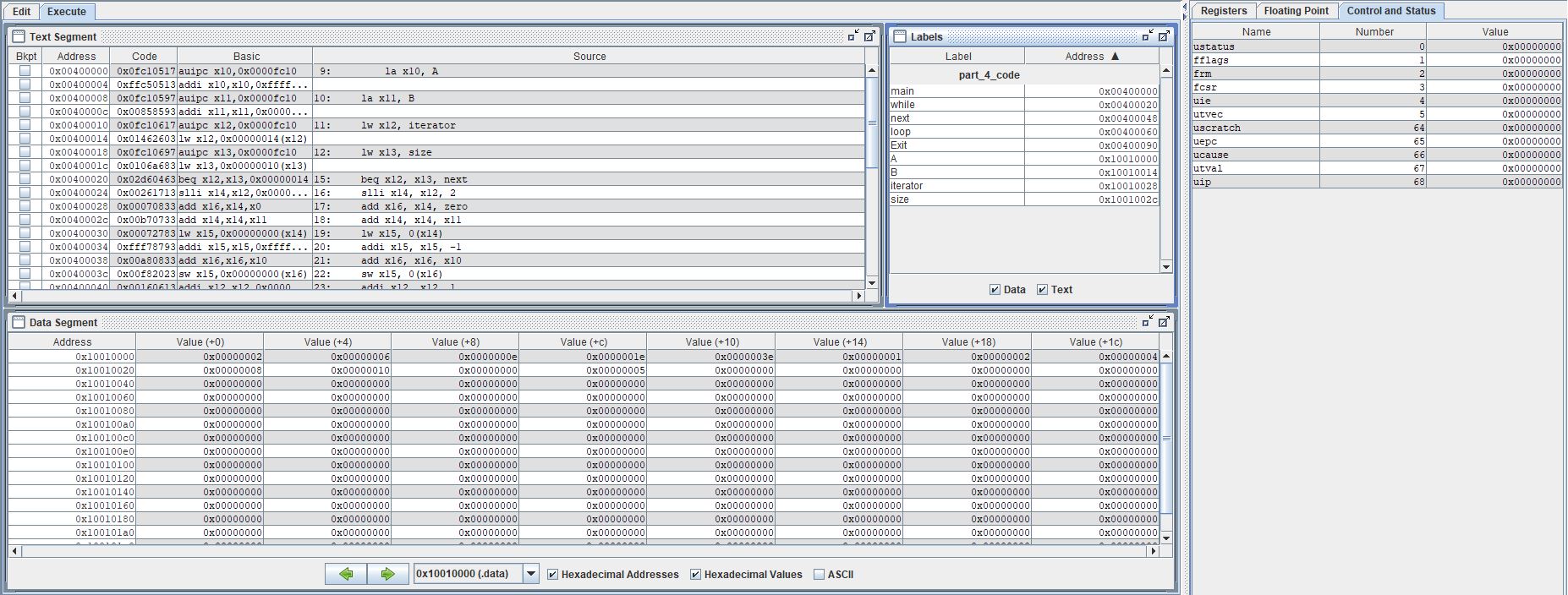
The screen shot above is the results of running the rars simulation program using the code for part 1 of the lab. From the labels tab, you can see Z is saved in memory. In addition, the value of Z in the end is 33 in decimal which is the correct sum of the given numbers.



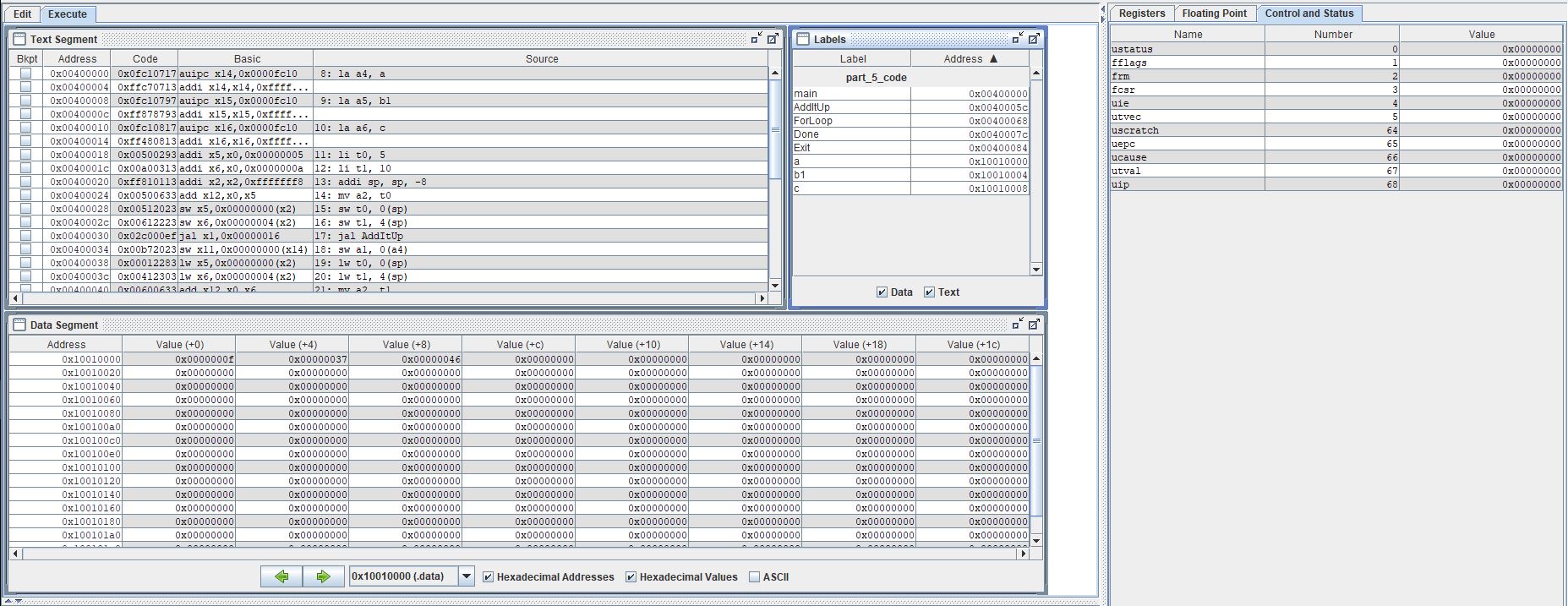
The screen shot above is the results of running the rars simulation program using the code for part 2 of the lab. From the labels tab, you can see that A, B, C, and Z are all saved in memory. Their values can also be seen in the data segment with Z giving the correct results.



The screen shot above is the results of running the rars simulation program using the code for part 3 of the lab. From the labels tab you can see that Z and I are both saved in memory. In the data segment the value of Z is shown as 78 in decimal which is correct number that Z should end on before the break in code.



The screen shot above is the results of running the rars simulation program using the code for part 4 of the lab. In the label tab you can see that the integer arrays A and B are both saved in memory. The data segment shows that the values of B stay the same while the values in array A change as they are supposed to.



The screen shot above is the results of running the rars simulation program using the code for part 5 of the lab. In the labels tab you can see a, b1, and c are all saved in memory. From the text segment you can see the stack was used. And the code ran correctly as we get a is 15, b1 is 55, and c is 70.

GITHUB LINK:

<https://github.com/ledonthuan/Labs>